



INSTITUTION:	Gdansk University of Technology, Faculty of Electronics, Telecommunications and Informatics, Department of Metrology and Optoelectronics
CITY:	Gdańsk
POSITION:	Assistant Professor
DISCIPLINE:	Electronic Engineering
POSTED:	08-12-2017
EXPIRES:	31-05-2018
WEBSITE:	http://praca.pg.gda.pl/oferta/1868/en
KEYWORDS:	metrology, microcontrollers, embedded systems

Tasks/Role

- * Conducting didactic courses (lectures, classes, laboratories) in the field of Electronics and Telecommunications in Polish or English.
- * Conducting research on fault diagnosis of metrology, microcontrollers, embedded systems.
- * Participation in scientific and didactic undertakings of the Department of Metrology and Optoelectronics.
- * Participation in the organization and development of didactic and science laboratories in the Department of Metrology and Optoelectronics.

Requirements

- * Ph.D. or Ph.D. with habilitation degree in the field of electronics.
- * Scientific track evidenced in the field of Electronics / Telecommunications, proven by publications, including also publications in international journals (indexed by JCR).
- * Participation in research projects.
- * Experience in delivering Electronics / Telecommunications courses at academic level.
- * Skills in team-work in scientific research.
- * Very good Polish language skills.
- * Good English language skills.

Documents



- * Curriculum Vitae.
 - * A cover letter declaring interest in the position posted.
 - * Documents proving professional qualifications required for the position — awarded academic research degrees, including Ph.D. or D.Sc., as well as experience-based degrees for services offered in the relevant field.
 - * Information on academic achievements (candidate with a degree of doctor hab. may present the documentation presented in the habilitation proceedings, after its possible updating).
 - * List of publications (arranged in chronological order).
 - * A reference letter one of independent researcher is welcome.
 - * Formal statement that Gdansk University of Technology will be the place of primary employment within the meaning of the Polish Higher Education Law (Dz.U. of 2016, No. 1842, Art. 2, Sec. 1 (33), <http://dziennikustaw.gov.pl/du/2016/1842>).
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COMPETITION ENDS: June 6, 2018

PLANNED DATE OF EMPLOYMENT: 01-10-2018

Documents should be e-mailed by 31 May 2018 to the address: kapsz@eti.pg.edu.pl.

*The contest may be closed without selecting a candidate.
We will contact the selected candidates.
All the documents sent will not be returned.*